



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jason Wei

Assignee: Rambus Inc.

Title: "System and Method for Selecting Optimal Data Transition Types
for Clock and Data Recovery"

Serial No.: 10/797,443 Filed: March 9, 2004

Examiner: Unknown Tel: Unknown

Docket No.: RA327.P.US Art Unit: Unknown

Mail Stop DD
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Pursuant to 37 C.F.R. §1.56, §1.97 and §1.98, Applicant brings the 5 documents listed on the enclosed form PTO-1449 to the Examiner's attention in the above-captioned application. Citation of the listed documents shall not be construed as:

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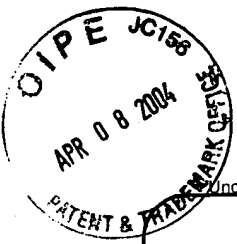
4.5.04

Date

Laurie Moreno

Respectfully submitted,

Arthur J. Behiel
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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/797,443	
	Filing Date	03/09/04	
	First Named Inventor	Jason Wei	
	Art Unit	Unknown	
	Examiner Name	Unknown	
Total Number of Pages in This Submission	3	Attorney Docket Number	RA327.P.US

ENCLOSURES (Check all that apply)		
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Firm or Individual name	Silicon Edge Law Group LLP Arthur J. Behiel, Patent Attorney
Signature	
Date	April 5, 2004

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Date	4.5.04

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U.S. Department of Commerce, Patent and Trademark Office	Serial No.: 10/797,443
	Filing Date: March 9, 2004
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventor: Jason Wei
"SYSTEM AND METHOD FOR SELECTING OPTIMAL DATA TRANSITION TYPES FOR CLOCK AND DATA RECOVERY"	Group Art Unit: Unknown
	Examiner Name: Unknown
	Attorney Docket No.: RA327.P.US

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	A						
	B						
	C						
	D						

Foreign Patent Documents

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	E							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

F	MARTIN, Aaron et al., "8 Gb/s Differential Simultaneous Bidirectional Link with 4mV 9ps Waveform Capture Diagnostic Capability." September 2003, IEEE International Solid-State Circuits Conference. Session 4/Clock Recovery and Backplane Transceivers/paper 4.5. 10 pages.
G	KASTURIA, Sanjay and WINTERS, Jack H., "Techniques for High-Speed Implementation of Nonlinear Cancellation." June 1991, IEEE Journal on Selected Areas in Communications Vol. 9. No. 5. Pages 711-717.
H	WINTERS, Jack H. and KASTURIA, Sanjay, "Adaptive Nonlinear Cancellation for High-Speed Fiber-Optic Systems." July 1992, IEEE Journal of Lightweight Technology, Vol. 10, No. 7. Pages 971-977.
I	PARHI, Keshab K., "Pipelining in Algorithms with Quantizer Loops." July 1991. IEEE Transactions on Circuits and Systems, Vol. 38, No. 7. Pages 745-754.
J	PARHI, Keshab K., "High-Speed Architectures for Algorithms with Quantizer Loops." August 1990. Proc. ISCAS. Pages 2357-2360.

Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.